Mar. 19. 2007 3:05PM Walker & Sako, LLP

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P. 1

AMENDMENT TRANSMITTAL LETTER						Attorney Docket No: N26532602E
Application Serial Number: 10/657,464		Filing Date: 09/08/2003		Examiner: Kim, Hong Chong		Group Art Unit: 2186
Invention: Data Processing Apparatus that Shares a Single Semiconductor Memory Circuit Among Multiple Data Processing Circuits						
TO THE COMMISSIONER OF PATENTS AND TRADEMARKS: Transmitted herewith is an amendment in the above-identified application. The fee has been calculated as shown below.						
CLAIMS AS AMENDED						
	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FÖR	NO. OF EXTRA CLAIMS PRESENT	rate	ADDITIONAL FEE
TOTAL CLAIMS	12	MINUS	20	0	\$50	\$ 0.00
INDEP. CLAIMS	. 2	MINUS	3	0	\$200	\$ 0.00
to and through, comprising an extension of the shortened statutory period of: one month (\$120) three months (\$1020) two months (\$450) four months (\$1590) A check in the amount of \$0.00 is attached. A check in the amount of \$0.00 is attached. Please charge any necessary additional fees or credit any overpayment to Deposit Account 50-2087. X No additional fee is required.						
Conditional Petition for Additional Extension of Time It is believed that the indicated items are timely filed. However, in the event an additional extension of time is required, please consider this a petition for extension of time under 37 C.F.R. §1.136(a). Please charge any fees that may be required to Deposit Account No. 50-2087.						
Date Darryl G. Walker, Reg. No. 43,232						
37 C.F.R. §1.8 I hereby certify that this correspondence is being						
[X] transmitted via facsimile to the United States Patent and Trademark Office to fax number:i-571-273-8300						
[] deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Box 1450, Alexandria, VA 22313-1450. Date of Deposit:						
Typed/Printed Name:	Darryl G. W	Valker				

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Uneme, Masakatsu

Serial No.: 10/657,464

Filed: September 8, 2003

Title: Data Processing Apparatus that Shares a Single Semiconductor Memory Circuit Among Multiple Data Processing Circuits

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Examiner: Kim, Hong Chong

AMENDMENT AFTER FINAL ACTION (37 C.F.R. §1.116)

Mail Stop AF Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313

Attorney Docket No.: N26532602E

A. <u>Introductory Comments</u>

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The following is submitted in response to the FINAL Office Action dated <u>December 19, 2006</u>, and is currently due <u>March 19, 2007</u>.